

	Type	L #	Hits	Search Text
1	BRS	L1	18	queue WITH (put\$3 enqueue\$3) WITH commit
2	BRS	L2	20	queue SAME (put\$3 enqueue\$3) WITH commit
3	BRS	L3	2	2 not 1
4	BRS	L4	20	queue SAME (put\$3 enqueue\$3) WITH commit
5	BRS	L5	1	queue SAME (put\$3 enqueue\$3) WITH commit
6	BRS	L6	2	(put\$3 enqueue\$3) WITH commit
7	BRS	L7	27	(put\$3 enqueue\$3) SAME commit
8	BRS	L8	10	(index\$4 link\$4 point\$4) SAME (put\$3 enqueue\$3) SAME commit
9	BRS	L10	1	(index\$4 link\$4 point\$4) SAME (put\$3 enqueue\$3) WITH commit.clm.
10	BRS	L9	14	(index\$4 link\$4 point\$4) SAME (put\$3 enqueue\$3) WITH commit
11	BRS	L11	22	(index\$4 link\$4 point\$4) SAME (put\$3 enqueue\$3) WITH commit
12	BRS	L12	4	(index\$4 link\$4 pointer pointing) SAME (put\$3 enqueue\$3) WITH commit
13	BRS	L13	270	(index\$4 link\$4 pointer pointing) WITH commit
14	BRS	L14	31	((index\$4 link\$4 pointer pointing) WITH commit).ti,ab,clm.
15	BRS	L15	2233	707/8
16	BRS	L16	44	13 and 15
17	BRS	L17	36	16 not 14
18	BRS	L18	199	(index\$4 link\$4 pointer pointing) WITH commit
19	BRS	L19	22	(index\$4 link\$4 pointer pointing) WITH commit.clm.
20	BRS	L20	61	707/8
21	BRS	L21	1	18 and 20
22	BRS	L22	342	(key index\$4 link\$4 pointer pointing) WITH commit
23	BRS	L23	1	22 and 20
24	BRS	L24	29	(key index\$4 link\$4 pointer pointing) WITH commit.clm.

	<b>DBs</b>	<b>Time Stamp</b>	<b>Comments</b>	<b>Error Definition</b>	<b>Errors</b>
1	USPAT	2005/11/30 08:47			
2	USPAT	2005/11/30 09:19			
3	USPAT	2005/11/30 09:20			
4	US-PGPUB	2005/11/30 09:20			
5	EPO; JPO; DERWENT; IBM_TDB	2005/11/30 09:52			
6	EPO; JPO; DERWENT; IBM_TDB	2005/11/30 09:53			
7	EPO; JPO; DERWENT; IBM_TDB	2005/11/30 09:59			
8	EPO; JPO; DERWENT; IBM_TDB	2005/11/30 11:19			
9	US-PGPUB	2005/11/30 11:20			
10	US-PGPUB	2005/11/30 11:25			
11	USPAT	2005/11/30 11:26			
12	USPAT	2005/11/30 11:35			
13	USPAT	2005/11/30 13:08			
14	USPAT	2005/11/30 12:26			
15	USPAT	2005/11/30 13:16			
16	USPAT	2005/11/30 13:17			
17	USPAT	2005/11/30 12:28			
18	US-PGPUB	2005/11/30 13:03			
19	US-PGPUB	2005/11/30 13:05			
20	US-PGPUB	2005/11/30 13:10			
21	US-PGPUB	2005/11/30 13:06			
22	US-PGPUB	2005/11/30 13:09			
23	US-PGPUB	2005/11/30 13:14			
24	US-PGPUB	2005/11/30 13:06			

	Type	L #	Hits	Search Text
25	BRS	L25	171	(key) WITH commit
26	BRS	L26	825	(key index\$4 link\$4 pointer pointing) SAME commit
27	BRS	L27	2667	707/1,8.ccls.
28	BRS	L28	126	707/8.ccls.
29	BRS	L29	44	26 and 27
30	BRS	L30	20	22 and 27
31	BRS	L31	0	707/8.ccls
32	BRS	L32	811	707/8.ccls.
33	BRS	L33	30	13 and 32
34	BRS	L34	7	33 not (16 14)
35	BRS	L35	15	25 and 32
36	BRS	L36	11	35 not (16 14 33)

	<b>DBs</b>	<b>Time Stamp</b>	<b>Comments</b>	<b>Error Definition</b>	<b>Errors</b>
25	USPAT	2005/11/30 13:15			
26	US-PGPUB	2005/11/30 13:09			
27	US-PGPUB	2005/11/30 13:10			
28	US-PGPUB	2005/11/30 13:10			
29	US-PGPUB	2005/11/30 13:11			
30	US-PGPUB	2005/11/30 13:15			
31	USPAT	2005/11/30 13:16			
32	USPAT	2005/11/30 13:16			
33	USPAT	2005/11/30 13:19			
34	USPAT	2005/11/30 13:18			
35	USPAT	2005/11/30 13:20			
36	USPAT	2005/11/30 13:20			



## Inventor Name Search Result

Your Search was:

Last Name = SIDDALL

First Name = PETER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09579677</u> ✓	Not Issued	41	05/26/2000	Administration of groups of computer programs,data processing systems, or system resources	SIDDALL, PETER
<u>09605589</u> ✓	Not Issued	161	06/28/2000	Method and apparatus for operating a computer system to enable a restart	SIDDALL, PETER
<u>09725578</u> ✓	6665814	150	11/29/2000	METHOD AND APPARATUS FOR PROVIDING SERIALIZATION SUPPORT FOR A COMPUTER SYSTEM	SIDDALL, PETER
<u>09790414</u> ✓	Not Issued	83	02/21/2001	Data processing system and method	SIDDALL, PETER
<u>09790415</u> ✓	6754842	150	02/21/2001	FACILITATING A RESTART OPERATION WITHIN A DATA PROCESSING SYSTEM	SIDDALL, PETER
<u>09909538</u> ✓APP	Not Issued	71	07/20/2001	Implementing MQI indexed queue support using coupling facility list structures	SIDDALL, PETER
<u>09912279</u> ✓	6842763	150	07/24/2001	METHOD AND APPARATUS FOR IMPROVING MESSAGE AVAILABILITY IN A SUBSYSTEM WHICH SUPPORTS SHARED MESSAGE QUEUES	SIDDALL, PETER
<u>10228615</u> ✓	6848037	150	08/27/2002	DATA PROCESSING ARRANGEMENT AND METHOD	SIDDALL, PETER
<u>10228636</u> ✓	6948093	150	08/27/2002	DATA PROCESSING ARRANGEMENT AND METHOD	SIDDALL, PETER
<u>10256093</u> ✓	Not Issued	95	09/26/2002	DATA RECOVERY SYSTEM	SIDDALL, PETER
<u>10660010</u> ✓	Not Issued	30	09/11/2003	Recovery from failures within data processing systems	SIDDALL, PETER
<u>60219889</u> copy	Not Issued	159	07/21/2000	Implementing mqj indexed queue support using coupling facility structures	SIDDALL, PETER
<u>60220685</u> ✓	Not Issued	159	07/25/2000	Method and apparatus for improving message availability in a subsystem which supports shared message queues	SIDDALL, PETER

Inventor Search Completed: No Records to Display.

Last Name

First Name

## Inventor Name Search Result

Your Search was:

Last Name = NICK

First Name = JEFFREY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
✓ 09677338	Not Issued	61	10/02/2000	Method and apparatus for enforcing capacity limitations in a logically partitioned system	NICK, JEFFREY M.
✓ 09677339	6963882	150	10/02/2000	METHOD AND APPARATUS FOR PROCESSING A LIST STRUCTURE	NICK, JEFFREY M.
✓ 09677341	6862595	150	10/02/2000	METHOD AND APPARATUS FOR IMPLEMENTING A SHARED MESSAGE QUEUE USING A LIST STRUCTURE	NICK, JEFFREY M.
✓ 09677454	6854021	150	10/02/2000	COMMUNICATIONS BETWEEN PARTITIONS WITHIN A LOGICALLY PARTITIONED COMPUTER	NICK, JEFFREY M.
✓ 09801993	Not Issued	95	03/08/2001	INTER-PARTITION MESSAGE PASSING METHOD, SYSTEM AND PROGRAM PRODUCT FOR MANAGING WORKLOAD IN A PARTITIONED PROCESSING ENVIRONMENT	NICK, JEFFREY M.
09909538 APP	Not Issued	71	07/20/2001	Implementing MQI indexed queue support using coupling facility list structures	NICK, JEFFREY M.
✓ 09968179	6859866	150	10/01/2001	SYNCHRONIZING PROCESSING OF COMMANDS INVOKED AGAINST DUPLEXED COUPLING FACILITY STRUCTURES	NICK, JEFFREY M.
✓ 09968185	Not Issued	71	10/01/2001	Dynamically determining whether to process requests synchronously or asynchronously	NICK, JEFFREY M.
✓ 09968242	6944787	150	10/01/2001	SYSTEM-MANAGED DUPLEXING OF COUPLING FACILITY STRUCTURES	NICK, JEFFREY M.
✓ 09968244	6954817	150	10/01/2001	PROVIDING AT LEAST ONE PEER CONNECTION BETWEEN A PLURALITY OF COUPLING FACILITIES TO COUPLE THE PLURALITY OF COUPLING FACILITIES	NICK, JEFFREY M.
✓ 09968248	Not Issued	94	10/01/2001	MANAGING THE STATE OF COUPLING FACILITY STRUCTURES	NICK, JEFFREY M.
✓ 10116985	Not	95	04/05/2002	MANAGING PROCESSING	NICK, JEFFREY M.

	Issued			ASSOCIATED WITH COUPLING FACILITY STRUCTURES	
✓ 10118113	6963994	150	04/05/2002	MANAGING CONNECTIONS TO COUPLING FACILITY STRUCTURES	NICK, JEFFREY M.
✓ 10140622	Not Issued	30	05/08/2002	Controlling the state of duplexing of coupling facility structures	NICK, JEFFREY M.
✓ 10141040	6615373	150	05/08/2002	METHOD, SYSTEM AND PROGRAM PRODUCTS FOR RESOLVING POTENTIAL DEADLOCKS	NICK, JEFFREY M.
✓ 11220296	Not Issued	20	09/06/2005	Method, system and program products for managing logical processors of a computing environment	NICK, JEFFREY M.
✓ 60219889	Not Issued	159	07/21/2000	Implementing mqj indexed queue support using coupling facility structures	NICK, JEFFREY M.
✓ 07221169	4979105	250	07/19/1988	METHOD AND APPARATUS FOR AUTOMATIC RECOVERY FROM EXCESSIVE SPIN LOOPS IN AN N-WAY MULTIPROCESSING SYSTEM	NICK, JEFFREY M.
✓ 07754816	Not Issued	166	09/04/1991	METHOD AND APPARATUS FOR RAPID DATA COPYING USING REASSIGNED BACKING PAGES	NICK, JEFFREY M.
✓ 07860330	5339405	150	03/30/1992	COMMAND QUIESCE FUNCTION	NICK, JEFFREY M.
✓ 07860378	5392397	150	03/30/1992	EXECUTION SYSTEM FOR USING FIRST AND SECOND COMMANDS TO RESERVE AND STORE SECOND COMMAND RELATED STATUS INFORMATION IN MEMORY PORTION RESPECTIVELY	NICK, JEFFREY M.
✓ 07860380	Not Issued	166	03/30/1992	IN A MULTIPROCESSING SYSTEM HAVING A COUPLING FACILITY, COMMUNICATING MESSAGES BETWEEN THE PROCESSORS AND THE COUPLING FACILITY IN EITHER A SYNCHRONOUS OPERATION OR AN ASYNCHRONOUS OPERATION	NICK, JEFFREY M.
✓ 07860489	5394554	150	03/30/1992	INTERDICTING I/O AND MASSAGING OPERATIONS FROM SENDING CENTRAL PROCESSING COMPLEX TO OTHER CENTRAL PROCESSING COMPLEXES AND TO I/O DEVICE IN MULTI-SYSTEM COMPLEX	NICK, JEFFREY M.
✓ 07860633	5410695	150	03/30/1992	APPARATUS AND METHOD FOR LIST MANAGEMENT IN A COUPLED DATA PROCESSING SYSTEM	NICK, JEFFREY M.
✓ 07860646	Not Issued	166	03/30/1992	MESSAGE PATH MECHANISM FOR MANAGING CONNECTIONS BETWEEN PROCESSORS AND A COUPLING FACILITY	NICK, JEFFREY M.
✓ 07860647	5394542	150	03/30/1992	CLEARING DATA OBJECTS USED TO	NICK, JEFFREY M.

✓				MAINTAIN STATE INFORMATION FOR SHARED DATA AT A LOCAL COMPLEX WHEN AT LEAST ONE MESSAGE PATH TO THE LOCAL COMPLEX CANNOT BE RECOVERED	
<u>07860655</u> ✓	Not Issued	166	03/30/1992	METHOD AND APPARATUS FOR PERFORMING CONDITIONAL OPERATIONS ON EXTERNALLY SHARED DATA	NICK, JEFFREY M.
<u>07860797</u> ✓	<u>5388266</u>	250	03/30/1992	MANAGEMENT OF DATA OBJECTS USED TO MAINTAIN STATE INFORMATION FOR SHARED DATA AT A LOCAL COMPLEX	NICK, JEFFREY M.
<u>07860800</u> ✓	<u>5331673</u>	150	03/30/1992	INTEGRITY OF DATA OBJECTS USED TO MAINTAIN STATE INFORMATION FOR SHARED DATA AT A LOCAL COMPLEX	NICK, JEFFREY M.
<u>07860803</u> ✓	<u>5317739</u>	150	03/30/1992	METHOD AND APPARATUS FOR COUPLING DATA PROCESSING SYSTEMS	NICK, JEFFREY M.
<u>07860805</u>	<u>5537574</u>	150	03/30/1992	SYSPLEX SHARED DATA COHERENCY METHOD	NICK, JEFFREY M.
<u>07860806</u> 2	<u>5493668</u>	150	03/30/1992	MULTIPLE PROCESSOR SYSTEM HAVING SOFTWARE FOR SELECTING SHARED CACHE ENTRIES OF AN ASSOCIATED CASTOUT CLASS FOR TRANSFER TO A DASD WITH ONE I/O OPERATION	NICK, JEFFREY M.
<u>07860807</u> ✓	<u>5457793</u>	150	03/30/1992	SOFTWARE CACHE MANAGEMENT OF A SHARED ELECTRONIC STORE IN A SYSPLEX	NICK, JEFFREY M.
<u>07860809</u> ✓	<u>5390328</u>	150	03/30/1992	DATA PROCESSING SYSTEM AND METHOD FOR PROVIDING NOTIFICATION TO A CENTRAL PROCESSOR OF STATE CHANGES FOR SHARED DATA STRUCTURE ON EXTERNAL STORAGE	NICK, JEFFREY M.
<u>07886273</u> ✓	Not Issued	166	05/20/1992	METHOD AND SYSTEM FOR LOCKING A PAGE OF REAL STORAGE USING A VIRTUAL ADDRESS	NICK, JEFFREY M.
<u>08021285</u> ✓	Not Issued	166	02/22/1993	AUTHORIZATION METHOD FOR CONDITIONAL COMMAND EXECUTION	NICK, JEFFREY M.
<u>08073909</u> ✓	<u>5761739</u>	150	06/08/1993	METHODS AND SYSTEMS FOR CREATING A STORAGE DUMP WITHIN A COUPLING FACILITY OF A MULTISYSTEM ENVIRONMENT	NICK, JEFFREY M.
<u>08146635</u> ✓	Not Issued	166	11/01/1993	METHOD AND SYSTEM FOR RECONFIGURING A STORAGE	NICK, JEFFREY M.

				STRUCTURE LOCATED WITHIN A STRUCTURE PROCESSING FACILITY	
<u>08146647</u> ✓	<u>5630050</u>	150	11/01/1993	METHOD AND SYSTEM FOR CAPTURING AND CONTROLLING ACCESS TO INFORMATION IN A COUPLING FACILITY	NICK, JEFFREY M.
<u>08146727</u> ✓	<u>5465359</u>	150	11/01/1993	METHOD AND SYSTEM FOR MANAGING DATA AND USERS OF DATA IN A DATA PROCESSING SYSTEM	NICK, JEFFREY M.
<u>08147351</u> ✓	<u>5416921</u>	150	11/03/1993	APPARATUS AND ACCOMPANYING METHOD FOR USE IN A SYSPLEX ENVIRONMENT FOR PERFORMING ESCALATED ISOLATION OF A SYSPLEX COMPONENT IN THE EVENT OF A FAILURE	NICK, JEFFREY M.
<u>08148707</u> ✓	<u>5544345</u>	150	11/08/1993	COHENRENCE CONTROLS FOR STORE-MULTIPLE SHARED DATA COORDINATED BY CACHE DIRECTORY ENTRIES IN A SHARED ELECTRONIC STORAGE	NICK, JEFFREY M.
<u>08276512</u> ✓	<u>5394539</u>	250	07/15/1994	METHOD AND APPARATUS FOR RAPID DATA COPYING REASSIGNED BACKING PAGES	NICK, JEFFREY M.
<u>08304458</u> ✓	<u>5581737</u>	150	09/12/1994	METHOD AND APPARATUS FOR EXPANSION, CONTRACTION, AND REAPPORTIONMENT OF STRUCTURED EXTERNAL STORAGE STRUCTURES	NICK, JEFFREY M.
<u>08304677</u> ✓	Not Issued	166	09/12/1994	METHOD AND SYSTEM FOR LOG MANAGEMENT IN A COUPLED DATA PROCESSING SYSTEM	NICK, JEFFREY M.
<u>08324447</u> ✓	<u>5463736</u>	150	10/18/1994	COUPLING FACILITY FOR RECEIVING COMMANDS FROM PLURALITY OF HOSTS FOR ACTIVATING SELECTED CONNECTION PATHS TO I/O DEVICES AND MAINTAINING STATUS THEREOF	NICK, JEFFREY M.
<u>08383532</u> ✓	<u>5742830</u>	150	02/01/1995	METHOD AND APPARATUS FOR PERFORMING CONDITIONAL OPERATIONS ON EXTERNALLY SHARED DATA	NICK, JEFFREY M.
<u>08408446</u> ✓	<u>5450590</u>	150	03/22/1995	AUTHORIZATION METHOD FOR CONDITIONAL COMMAND EXECUTION	NICK, JEFFREY M.
<u>08420893</u> ✓	<u>5561809</u>	150	04/11/1995	IN A MULTIPROCESSING SYSTEM HAVING A COUPLING FACILITY, COMMUNICATING MESSAGES BETWEEN THE PROCESSORS AND	NICK, JEFFREY M.

				THE COUPLING FACILITY IN EITHER A SYNCHRONOUS OPERATION OR AN ASYNCHRONOUS OPERATION	
<u>08439269</u> ✓	<u>5604863</u>	150	05/09/1995	A METHOD FOR COORDINATING EXECUTING PROGRAMS IN A DATA PROCESSING SYSTEM	NICK, JEFFREY M.

[Search and Display More Records.](#)

---

	<b>Last Name</b>	<b>First Name</b>	
<b>Search Another: Inventor</b>	<input type="text" value="NICK"/>	<input type="text" value="JEFFREY"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

## Inventor Name Search Result

Your Search was:

Last Name = WARNES

First Name = JAMES

Application#	Patent#	Status	Date Filed	Title	Inventor Name
✓ 60220685	Not Issued	159	07/25/2000	Method and apparatus for improving message availability in a subsystem which supports shared message queues	WARNES, JAMES
✓ 09677339	6963882 ✓	150	10/02/2000	METHOD AND APPARATUS FOR PROCESSING A LIST STRUCTURE	WARNES, JAMES H.
✓ 09677341	6862595	150	10/02/2000	METHOD AND APPARATUS FOR IMPLEMENTING A SHARED MESSAGE QUEUE USING A LIST STRUCTURE	WARNES, JAMES H.
09909538 APP	Not Issued	71	07/20/2001	Implementing MQI indexed queue support using coupling facility list structures	WARNES, JAMES H.
✓ 09912279	6842763 ✓	150	07/24/2001	METHOD AND APPARATUS FOR IMPROVING MESSAGE AVAILABILITY IN A SUBSYSTEM WHICH SUPPORTS SHARED MESSAGE QUEUES	WARNES, JAMES H.
✓ 11005805	Not Issued	30	12/07/2004	Browsing a list of data items	WARNES, JAMES H.
60219889 PATENT DOC.	Not Issued	159	07/21/2000	Implementing mqj indexed queue support using coupling facility structures	WARNES, JAMES H.
✓ 06781844	4809157	150	09/30/1985	DYNAMIC ASSIGNMENT OF AFFINITY FOR VECTOR TASKS	WARNES, JAMES H.
✓ 08304677	Not Issued	166	09/12/1994	METHOD AND SYSTEM FOR LOG MANAGEMENT IN A COUPLED DATA PROCESSING SYSTEM	WARNES, JAMES H.
✓ 08632683	5737600	150	04/15/1996	METHOD AND SYSTEM FOR LOG MANAGEMENT IN A COUPLED DATA PROCESSING SYSTEM	WARNES, JAMES H.
✓ 09725578	6665814	150	11/29/2000	METHOD AND APPARATUS FOR PROVIDING SERIALIZATION SUPPORT FOR A COMPUTER SYSTEM	WARNES, JAMES HENRY

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
WARNES	JAMES	Search

## Inventor Name Search Result

Your Search was:

Last Name = HOPEWELL

First Name = PAUL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09909538</u> <i>APP</i>	Not Issued	71	07/20/2001	Implementing MQI indexed queue support using coupling facility list structures	HOPEWELL, PAUL
<input checked="" type="checkbox"/> <u>10228615</u>	<u>6848037</u>	150	08/27/2002	DATA PROCESSING ARRANGEMENT AND METHOD	HOPEWELL, PAUL
<input checked="" type="checkbox"/> <u>10228636</u>	<u>6948093</u>	150	08/27/2002	DATA PROCESSING ARRANGEMENT AND METHOD	HOPEWELL, PAUL
<input checked="" type="checkbox"/> <u>10256093</u>	Not Issued	95	09/26/2002	DATA RECOVERY SYSTEM	HOPEWELL, PAUL
<input checked="" type="checkbox"/> <u>10660010</u>	Not Issued	30	09/11/2003	Recovery from failures within data processing systems	HOPEWELL, PAUL
<input checked="" type="checkbox"/> <u>11005805</u>	Not Issued	30	12/07/2004	Browsing a list of data items	HOPEWELL, PAUL
<input checked="" type="checkbox"/> <u>11255204</u>	Not Issued	20	10/20/2005	Method, apparatus, computer program and computer program product for adjusting the frequency at which data is backed up	HOPEWELL, PAUL
<u>60219889</u> <i>PATENTY Docu</i>	Not Issued	159	07/21/2000	Implementing mqj indexed queue support using coupling facility structures	HOPEWELL, PAUL
<input checked="" type="checkbox"/> <u>11144931</u>	Not Issued	19	06/03/2005	System and method for operating a wind farm under high wind speed conditions	HOPEWELL, PAUL DAVID
<input checked="" type="checkbox"/> <u>11172769</u>	Not Issued	30	06/30/2005	System and method for controlling effective wind farm power output	HOPEWELL, PAUL DAVID

Inventor Search Completed: No Records to Display.

**Search Another: Inventor**

**Last Name**

**First Name**

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

## Inventor Name Search Result

Your Search was:

Last Name = KETTLEY

First Name = PAUL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
✓ <u>09579677</u>	Not Issued	41	05/26/2000	Administration of groups of computer programs,data processing systems, or system resources	KETTLEY, PAUL
✓ <u>09605589</u>	Not Issued	(161)	06/28/2000	Method and apparatus for operating a computer system to enable a restart	KETTLEY, PAUL
✓ <u>09725578</u>	6665814	150	11/29/2000	METHOD AND APPARATUS FOR PROVIDING SERIALIZATION SUPPORT FOR A COMPUTER SYSTEM	KETTLEY, PAUL
✓ <u>09790414</u>	Not Issued	83	02/21/2001	Data processing system and method	KETTLEY, PAUL
✓ <u>09790415</u>	6754842	150	02/21/2001	FACILITATING A RESTART OPERATION WITHIN A DATA PROCESSING SYSTEM	KETTLEY, PAUL
<u>09909538</u> A-PP	Not Issued	71	07/20/2001	Implementing MQI indexed queue support using coupling facility list structures	KETTLEY, PAUL
✓ <u>09912279</u>	6842763	150	07/24/2001	METHOD AND APPARATUS FOR IMPROVING MESSAGE AVAILABILITY IN A SUBSYSTEM WHICH SUPPORTS SHARED MESSAGE QUEUES	KETTLEY, PAUL
✓ <u>10228615</u>	6848037	150	08/27/2002	DATA PROCESSING ARRANGEMENT AND METHOD	KETTLEY, PAUL
✓ <u>10228636</u>	6948093	150	08/27/2002	DATA PROCESSING ARRANGEMENT AND METHOD	KETTLEY, PAUL
✓ <u>10256093</u>	Not Issued	95	09/26/2002	DATA RECOVERY SYSTEM	KETTLEY, PAUL
✓ <u>10660010</u>	Not Issued	30	09/11/2003	Recovery from failures within data processing systems	KETTLEY, PAUL
✓ <u>11168689</u>	Not Issued	30	06/28/2005	Controlling a transmission cache in a networked file system	KETTLEY, PAUL
✓ <u>11255204</u>	Not Issued	20	10/20/2005	Method, apparatus, computer program and computer program product for adjusting the frequency at which data is backed up	KETTLEY, PAUL
✓ <u>60183861</u>	Not Issued	159	02/22/2000	Accelerating resource manager restart via force commit on incomplete units of work	KETTLEY, PAUL

✓ 60183925	Not Issued	159	02/22/2000	Flexible mechanism for controlling access to resources within a group of co-operating queue managers	KETTLEY, PAUL
60219889 <i>primary</i>	Not Issued	159	07/21/2000	Implementing mqj indexed queue support using coupling facility structures	KETTLEY, PAUL
60220685	Not Issued	159	07/25/2000	Method and apparatus for improving message availability in a subsystem which supports shared message queues	KETTLEY, PAUL

Inventor Search Completed: No Records to Display.

**Search Another: Inventor**

Last Name	First Name
<input type="text" value="KETTLEY"/>	<input type="text" value="PAUL"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

☐ Author Search

[BROWSE](#)

[SEARCH](#)

[IEEE XPLORE GUIDE](#)

[SUPPORT](#)



**OPTION 1**

**Quick Find an Author:**

Enter a name to locate articles written by that author.



Example: Enter Lockett S to obtain a list of authors with the last name Lockett and the first initial S.

Select a name to view articles written by that author

[Siddall G.](#)

[Siddall M.](#)

[Siddall M. B.](#)

[Siddall R.](#)

[Siddall R. B.](#)



**OPTION 2**

**Browse alphabetically**

Select a letter from the list.

[A](#) [B](#) [C](#) [D](#) [E](#) [F](#) [G](#) [H](#) [I](#) [J](#) [K](#) [L](#) [M](#) [N](#) [O](#) [P](#) [Q](#) [R](#) [S](#) [T](#) [U](#) [V](#) [W](#) [X](#) [Y](#) [Z](#)

**Search Results**

[BROWSE](#)

[SEARCH](#)

[IEEE XPLORE GUIDE](#)

[SUPPORT](#)

Results for "(nick j. m.<in>au)"

Your search matched 2 of 1263585 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

 [e-mail](#)  [printer friendly](#)

» **Search Options**

[View Session History](#)

[New Search](#)

**Modify Search**

(nick j. m.<in>au)



☐ Check to search only within this results set

Display Format: ☒ Citation ☐ Citation & Abstract

» **Key**

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

**Select Article Information**

- ☐ 1. **Grid services for distributed system integration**  
Foster, I.; Kesselman, C.; Nick, J.M.; Tuecke, S.;  
Computer  
Volume 35, Issue 6, June 2002 Page(s):37 - 46  
Digital Object Identifier 10.1109/MC.2002.1009167  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(542 KB\)](#) IEEE JNL
- ☐ 2. **Overview of IBM system/390 parallel sysplex-a commercial parallel processing system**  
Nick, J.M.; Jen-Yao Chung; Bowen, N.S.;  
Parallel Processing Symposium, 1996., Proceedings of IPPS '96, The 10th International  
15-19 April 1996 Page(s):488 - 495  
Digital Object Identifier 10.1109/IPPS.1996.508100  
[AbstractPlus](#) | Full Text: [PDF\(840 KB\)](#) IEEE CNF



 Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "(key<and>queue)<and>commit"

Your search matched **1051** of **1263585** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.


 e-mail  printer friendly

» Search Options

[View Session History](#)

[New Search](#)

Modify Search



☐ Check to search only within this results set

Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Select Article Information

View: [1-25](#) | [26-50](#) | [51-75](#) | [76-100](#)

- ☐ 1. **Investigating component-based maintenance and the effect of software evolution: a reengineering approach using data clustering**  
Burd, E.; Munro, M.;  
Software Maintenance, 1998. Proceedings. International Conference on  
16-20 Nov. 1998 Page(s):199 - 207  
Digital Object Identifier 10.1109/ICSM.1998.738509  
[AbstractPlus](#) | Full Text: [PDF](#)(160 KB) IEEE CNF
- ☐ 2. **A systematic methodology to compute the architectural vulnerability factors for a high-performance microprocessor**  
Mukherjee, S.S.; Weaver, C.; Emer, J.; Reinhardt, S.K.; Austin, T.;  
Microarchitecture, 2003. MICRO-36. Proceedings. 36th Annual IEEE/ACM International Symposium on  
2003 Page(s):29 - 40  
Digital Object Identifier 10.1109/MICRO.2003.1253181  
[AbstractPlus](#) | Full Text: [PDF](#)(374 KB) IEEE CNF
- ☐ 3. **IEEE guide to the POSIX Open System Environment (OSE)**  
IEEE Std 1003.0-1995  
29 Dec. 1995  
[AbstractPlus](#) | Full Text: [PDF](#)(1724 KB) IEEE STD
- ☐ 4. **Inheritance of synchronization and recovery properties in Avalon/C++**  
Detlefs, D.L.; Herlihy, M.P.; Wing, J.M.;  
Computer  
Volume 21, Issue 12, Dec. 1988 Page(s):57 - 69  
Digital Object Identifier 10.1109/2.16189  
[AbstractPlus](#) | Full Text: [PDF](#)(920 KB) IEEE JNL
- ☐ 5. **IEEE Standard for Modeling and Simulation [M and S] High Level Architecture [HLA] - Federate Interface Specification**  
IEEE Std 1516.1-2000  
2001 Page(s):i - 467  
[AbstractPlus](#) | Full Text: [PDF](#)(2276 KB) IEEE STD
- ☐ 6. **Transient-fault recovery for chip multiprocessors**  
Gomaa, M.; Scarbrough, C.; Vijaykumar, T.N.; Pomeranz, I.;  
Computer Architecture, 2003. Proceedings. 30th Annual International Symposium on  
9-11 June 2003 Page(s):98 - 109  
Digital Object Identifier 10.1109/ISCA.2003.1206992  
[AbstractPlus](#) | Full Text: [PDF](#)(443 KB) IEEE CNF

- ☐ 7. **Theories and models for Internet quality of service**  
Firoiu, V.; Le Boudec, J.-Y.; Towsley, D.; Zhi-Li Zhang;  
Proceedings of the IEEE  
Volume 90, Issue 9, Sept. 2002 Page(s):1565 - 1591  
Digital Object Identifier 10.1109/JPROC.2002.802002  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(600 KB\)](#) | Full Text: [HTML](#) IEEE JNL
  
- ☐ 8. **Publish/subscribe in NonStop SQL: transactional streams in a relational context**  
Hanlon, M.; Klein, J.; Van der Linden, R.; Zeller, H.;  
Data Engineering, 2004. Proceedings. 20th International Conference on  
30 March-2 April 2004 Page(s):821 - 824  
Digital Object Identifier 10.1109/ICDE.2004.1320056  
[AbstractPlus](#) | Full Text: [PDF\(251 KB\)](#) IEEE CNF
  
- ☐ 9. **PARLOG and its applications**  
Clark, K.L.;  
Software Engineering, IEEE Transactions on  
Volume 14, Issue 12, Dec. 1988 Page(s):1792 - 1804  
Digital Object Identifier 10.1109/32.9064  
[AbstractPlus](#) | Full Text: [PDF\(1188 KB\)](#) IEEE JNL
  
- ☐ 10. **Opportunistic transient-fault detection**  
Gomaa, M.A.; Vijaykumar, T.N.;  
Computer Architecture, 2005. ISCA '05. Proceedings. 32nd International Symposium on  
4-8 June 2005 Page(s):172 - 183  
Digital Object Identifier 10.1109/ISCA.2005.38  
[AbstractPlus](#) | Full Text: [PDF\(160 KB\)](#) IEEE CNF
  
- ☐ 11. **Mitigating Inductive Noise in SMT Processors**  
El-Essawy, W.; Albonesi, D.H.;  
Low Power Electronics and Design, 2004. ISLPED '04. Proceedings of the 2004 International  
Symposium on  
2004 Page(s):332 - 337  
[AbstractPlus](#) | Full Text: [PDF\(808 KB\)](#) IEEE CNF
  
- ☐ 12. **Network QoS assurance in a multi-layer adaptive resource management scheme for mission-critical applications using the CORBA middleware framework**  
Dasarathy, B.; Gadgil, S.; Vaidyanathan, R.; Parmeswaran, K.; Coan, B.; Conarty, M.; Bhanot, V.;  
Real Time and Embedded Technology and Applications Symposium, 2005. RTAS 2005. 11th  
IEEE  
7-10 March 2005 Page(s):246 - 255  
Digital Object Identifier 10.1109/RTAS.2005.34  
[AbstractPlus](#) | Full Text: [PDF\(184 KB\)](#) IEEE CNF
  
- ☐ 13. **Pal:a new fossil ollector for time warp**  
Vee, V.-Y.; Wen-Jing Hsu;  
Parallel and Distributed Simulation, 2002. Proceedings. 16th Workshop on  
12-15 May 2002 Page(s):31 - 38  
[AbstractPlus](#) | Full Text: [PDF\(389 KB\)](#) IEEE CNF
  
- ☐ 14. **Implicitly-multithreaded processors**  
Il Park; Falsafi, B.; Vijaykumar, T.N.;  
Computer Architecture, 2003. Proceedings. 30th Annual International Symposium on  
9-11 June 2003 Page(s):39 - 50  
Digital Object Identifier 10.1109/ISCA.2003.1206987  
[AbstractPlus](#) | Full Text: [PDF\(462 KB\)](#) IEEE CNF
  
- ☐ 15. **Unification of replication and transaction processing in three-tier architectures**  
Zhao, W.; Moser, L.E.; Melliari-Smith, P.M.;  
Distributed Computing Systems, 2002. Proceedings. 22nd International Conference on  
2-5 July 2002 Page(s):290 - 297  
Digital Object Identifier 10.1109/ICDCS.2002.1022266  
[AbstractPlus](#) | Full Text: [PDF\(334 KB\)](#) IEEE CNF

- ☐ **16. Testing the dependability and performance of group communication based database replication protocols**  
Sousa, A.; Pereira, J.; Soares, L.; Correia, A., Jr.; Rocha, L.; Oliveira, R.; Moura, F.; Dependable Systems and Networks, 2005. DSN 2005. Proceedings. International Conference on  
28 June-1 July 2005 Page(s):792 - 801  
Digital Object Identifier 10.1109/DSN.2005.90  
[AbstractPlus](#) | Full Text: [PDF\(424 KB\)](#) IEEE CNF
  
- ☐ **17. Accurate Modeling of Aggressive Speculation in Modern Microprocessor Architectures**  
Modi, H.; Spracklen, L.; Yuan Chou; Abraham, S.G.; Modeling, Analysis, and Simulation of Computer and Telecommunication Systems, 2005. 13th IEEE International Symposium on  
27-29 Sept. 2005 Page(s):75 - 84  
Digital Object Identifier 10.1109/MASCOTS.2005.12  
[AbstractPlus](#) | Full Text: [PDF\(240 KB\)](#) IEEE CNF
  
- ☐ **18. /spl mu/sik - a micro-kernel for parallel/distributed simulation systems**  
Perumalla, K.S.; Principles of Advanced and Distributed Simulation, 2005. PADS 2005. Workshop on  
1-3 June 2005 Page(s):59 - 68  
Digital Object Identifier 10.1109/PADS.2005.1  
[AbstractPlus](#) | Full Text: [PDF\(160 KB\)](#) IEEE CNF
  
- ☐ **19. Integrated quality of service (QoS) management in service-oriented enterprise architectures**  
Wang, G.; Chen, A.; Wang, C.; Fung, C.; Uczekaj, S.; Enterprise Distributed Object Computing Conference, 2004. EDOC 2004. Proceedings. Eighth IEEE International  
2004 Page(s):21 - 32  
Digital Object Identifier 10.1109/EDOC.2004.1342502  
[AbstractPlus](#) | Full Text: [PDF\(361 KB\)](#) IEEE CNF
  
- ☐ **20. A comparative evaluation of transparent scaling techniques for dynamic content servers**  
Amza, C.; Cox, A.L.; Zwaenepoel, W.; Data Engineering, 2005. ICDE 2005. Proceedings. 21st International Conference on  
5-8 April 2005 Page(s):230 - 241  
Digital Object Identifier 10.1109/ICDE.2005.6  
[AbstractPlus](#) | Full Text: [PDF\(200 KB\)](#) IEEE CNF
  
- ☐ **21. Reducing datapath energy through the isolation of short-lived operands**  
Ponomarev, D.; Kucuk, G.; Ergin, O.; Ghose, K.; Parallel Architectures and Compilation Techniques, 2003. PACT 2003. Proceedings. 12th International Conference on  
27 Sept.-1 Oct. 2003 Page(s):258 - 268  
Digital Object Identifier 10.1109/PACT.2003.1238021  
[AbstractPlus](#) | Full Text: [PDF\(297 KB\)](#) IEEE CNF
  
- ☐ **22. Integration of call signaling and resource management for IP telephony**  
Goyal, P.; Greenberg, A.; Kalmanek, C.R.; Marshall, W.T.; Mishra, P.; Nortz, D.; Ramakrishnan, K.K.; Internet Computing, IEEE  
Volume 3, Issue 3, May-June 1999 Page(s):44 - 52  
Digital Object Identifier 10.1109/4236.769422  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(212 KB\)](#) IEEE JNL
  
- ☐ **23. Complexity-effective reorder buffer designs for superscalar processors**  
Kucuk, G.; Ponomarev, D.V.; Ergin, O.; Ghose, K.; Computers, IEEE Transactions on  
Volume 53, Issue 6, June 2004 Page(s):653 - 665  
Digital Object Identifier 10.1109/TC.2004.5  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1376 KB\)](#) IEEE JNL

**24. Concurrent algorithms for real-time memory management**

Ford, R.;

Software, IEEE

Volume 5, Issue 5, Sept. 1988 Page(s):10 - 23

Digital Object Identifier 10.1109/52.7940

[AbstractPlus](#) | Full Text: [PDF](#)(1064 KB) IEEE JNL

**25. Efficient execution of Time Warp programs on heterogeneous, NOW platforms**

Carothers, C.D.; Fujimoto, R.M.;

Parallel and Distributed Systems, IEEE Transactions on

Volume 11, Issue 3, March 2000 Page(s):299 - 317

Digital Object Identifier 10.1109/71.841745

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(656 KB) IEEE JNL

**View:** [1-25](#) | [26-50](#) | [51-75](#) | [76-100](#)

[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

© Copyright 2005 IEEE – All Rights Reserved

 Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "((key <and> queue <and> commit)<in>metadata)"

Your search matched 1 of 1263585 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.


 e-mail  printer friendly

» Search Options

[View Session History](#)

[New Search](#)

Modify Search



☐ Check to search only within this results set

Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. **Cooperative adjusted RED in Diffserv network**  
Qian Wang; Keping Long; Shiduan Cheng; Runtong Zhang;  
Info-tech and Info-net, 2001. Proceedings. ICII 2001 - Beijing. 2001 International Conferences on  
Volume 2, 29 Oct.-1 Nov. 2001 Page(s):205 - 210 vol.2  
Digital Object Identifier 10.1109/ICII.2001.983578  
[AbstractPlus](#) | Full Text: [PDF](#)(523 KB) IEEE CNF



**Search Results**

**BROWSE**

**SEARCH**

**IEEE XPLORE GUIDE**

**SUPPORT**

Results for "( ( index<in>metadata ) <and> ( queue<in>metadata ) )<and> ( commit<in>..."

Your search matched **0** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

 [e-mail](#)  [printer friendly](#)

» **Search Options**

[View Session History](#)

[New Search](#)

**Modify Search**

(( ( index<in>metadata ) <and> ( queue<in>metadata ) )<and> ( commit<in>metadata ) )>>

☐ Check to search only within this results set

**Display Format:** ☒ Citation ☐ Citation & Abstract

» **Key**

**IEEE JNL** IEEE Journal or Magazine

**IEE JNL** IEE Journal or Magazine

**IEEE CNF** IEEE Conference Proceeding

**IEE CNF** IEE Conference Proceeding

**IEEE STD** IEEE Standard

**No results were found.**

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revising your search.



**OPTION 1**

Enter keywords or phrases, select fields, and select operators



in Abstract

in Abstract

in Abstract

» Note: If you use all three search boxes, the entries in the first two boxes take precedence over the entry in the third box.



**OPTION 2**

Enter keywords, phrases, or a Boolean expression



» Note: You may use the search operators <and> or <or> without the start and end brackets <>.

» Learn more about [Field Codes](#), [Search Examples](#), and [Search Operators](#)

» **Publications**

☒ Select publications

- ☒ IEEE Periodicals
- ☒ IEE Periodicals
- ☒ IEEE Conference Proceedings
- ☒ IEE Conference Proceedings
- ☒ IEEE Standards

» **Other Resources** (Available for Purchase)

- ☒ IEEE Books

» **Select date range**

- ☐ Search latest content update (21 Nov 2005)
- ☒ From year  to

» **Display Format**

- ☒ Citation
- ☐ Citation & Abstract

» **Organize results**

Maximum  results per page

Display  results per page

Sort by  In  order

 Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "(index<and>queue)<and>commit"

Your search matched **841** of **1263585** documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

 e-mail  printer friendly

» Search Options

[View Session History](#)

[New Search](#)

Modify Search

(index<and>queue)<and>commit 

☐ Check to search only within this results set

Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Select Article Information

View: [1-25](#) | [26-50](#) | [51-75](#) | [76-100](#)

- ☐ 1. **IEEE standard for information technology-telecommunications and information exchange between systems-local and metropolitan area networks-specific requirements-part 17: resilient packet ring (RPR) access method and physical layer specifications**  
IEEE Std 802.17-2004  
2004 Page(s):0\_1 - 664  
[AbstractPlus](#) | Full Text: [PDF](#)(5472 KB) IEEE STD
- ☐ 2. **Scalable hardware memory disambiguation for high ILP processors**  
Sethumadhavan, S.; Desikan, R.; Burger, D.; Moore, C.R.; Keckler, S.W.;  
Microarchitecture, 2003. MICRO-36. Proceedings. 36th Annual IEEE/ACM International Symposium on  
2003 Page(s):399 - 410  
Digital Object Identifier 10.1109/MICRO.2003.1253244  
[AbstractPlus](#) | Full Text: [PDF](#)(347 KB) IEEE CNF
- ☐ 3. **Inheritance of synchronization and recovery properties in Avalon/C++**  
Dettefs, D.L.; Herlihy, M.P.; Wing, J.M.;  
Computer  
Volume 21, Issue 12, Dec. 1988 Page(s):57 - 69  
Digital Object Identifier 10.1109/2.16189  
[AbstractPlus](#) | Full Text: [PDF](#)(920 KB) IEEE JNL
- ☐ 4. **1987 ACM SIGMETRICS Conference on Measurement and Modeling of Computer Systems**  
Software Engineering, IEEE Transactions on  
Volume 14, Issue 4, Apr 1988  
Digital Object Identifier 10.1109/32.4674  
[AbstractPlus](#) | Full Text: [PDF](#)(2728 KB) IEEE JNL
- ☐ 5. **Better exploration of region-level value locality with integrated computation reuse and value prediction**  
Youfeng Wu; Dong-Yuan Chen; Fang, J.;  
Computer Architecture, 2001. Proceedings. 28th Annual International Symposium on  
30 June-4 July 2001 Page(s):98 - 108  
Digital Object Identifier 10.1109/ISCA.2001.937437  
[AbstractPlus](#) | Full Text: [PDF](#)(192 KB) IEEE CNF
- ☐ 6. **Scalable hardware memory disambiguation for high-ILP processors**  
Sethumadhavan, S.; Desikan, R.; Burger, D.; Moore, C.R.; Keckler, S.W.;  
Micro, IEEE  
Volume 24, Issue 6, Nov-Dec 2004 Page(s):118 - 127  
Digital Object Identifier 10.1109/MM.2004.87  
[AbstractPlus](#) | Full Text: [PDF](#)(168 KB) IEEE JNL

- ☐ **7. Reducing datapath energy through the isolation of short-lived operands**  
Ponomarev, D.; Kucuk, G.; Ergin, O.; Ghose, K.;  
Parallel Architectures and Compilation Techniques, 2003. PACT 2003. Proceedings. 12th International Conference on  
27 Sept.-1 Oct. 2003 Page(s):258 - 268  
Digital Object Identifier 10.1109/PACT.2003.1238021  
[AbstractPlus](#) | Full Text: [PDF\(297 KB\)](#) IEEE CNF
  
- ☐ **8. Late allocation and early release of physical registers**  
Monreal, T.; Vinals, V.; Gonzalez, J.; Gonzalez, A.; Valero, M.;  
Computers, IEEE Transactions on  
Volume 53, Issue 10, Oct. 2004 Page(s):1244 - 1259  
Digital Object Identifier 10.1109/TC.2004.79  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1696 KB\)](#) IEEE JNL
  
- ☐ **9. Concurrency control for mixed transactions in real-time databases**  
Lee, V.C.S.; Kwok-wa Lam; Sheung-Lun Hung;  
Computers, IEEE Transactions on  
Volume 51, Issue 7, July 2002 Page(s):821 - 834  
Digital Object Identifier 10.1109/TC.2002.1017702  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(760 KB\)](#) IEEE JNL
  
- ☐ **10. Speculative locking protocols to improve performance for distributed database systems**  
Krishna Reddy, P.; Kitsuregawa, M.;  
Knowledge and Data Engineering, IEEE Transactions on  
Volume 16, Issue 2, Feb. 2004 Page(s):154 - 169  
Digital Object Identifier 10.1109/TKDE.2004.1269595  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(520 KB\)](#) IEEE JNL
  
- ☐ **11. Using an operand file to save energy and to decouple commit resources**  
Reinman, G.;  
Computers and Digital Techniques, IEE Proceedings-  
Volume 152, Issue 5, September 2005 Page(s):666 - 678  
[AbstractPlus](#) | Full Text: [PDF\(571 KB\)](#) IEE JNL
  
- ☐ **12. A programmable hardware path profiler**  
Kapil Vaswani; Thazhuthaveetil, M.J.; Srikant, Y.N.;  
Code Generation and Optimization, 2005. CGO 2005. International Symposium on  
20-23 March 2005 Page(s):217 - 228  
Digital Object Identifier 10.1109/CGO.2005.3  
[AbstractPlus](#) | Full Text: [PDF\(464 KB\)](#) IEEE CNF
  
- ☐ **13. IEEE guide to the POSIX Open System Environment (OSE)**  
IEEE Std 1003.0-1995  
29 Dec. 1995  
[AbstractPlus](#) | Full Text: [PDF\(1724 KB\)](#) IEEE STD
  
- ☐ **14. Autonomic Microprocessor Execution via Self-Repairing Arrays**  
Bower, F.A.; Ozev, S.; Sorin, D.J.;  
Dependable and Secure Computing, IEEE Transactions on  
Volume 2, Issue 4, Oct.-Dec. 2005 Page(s):297 - 310  
Digital Object Identifier 10.1109/TDSC.2005.44  
[AbstractPlus](#) | Full Text: [PDF\(1184 KB\)](#) IEEE JNL
  
- ☐ **15. Isolating short-lived operands for energy reduction**  
Ponomarev, D.; Kucuk, G.; Ergin, O.; Ghose, K.;  
Computers, IEEE Transactions on  
Volume 53, Issue 6, June 2004 Page(s):697 - 709  
Digital Object Identifier 10.1109/TC.2004.11  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1456 KB\)](#) IEEE JNL
  
- ☐ **16. Quantifying instruction criticality**

Tune, E.S.; Tullsen, D.M.; Calder, B.;  
Parallel Architectures and Compilation Techniques, 2002. Proceedings. 2002 International  
Conference on  
22-25 Sept. 2002 Page(s):104 - 113  
Digital Object Identifier 10.1109/PACT.2002.1106008  
[AbstractPlus](#) | Full Text: [PDF](#)(379 KB) IEEE CNF

- ☐ **17. Scalable load and store processing in latency tolerant processors**  
Gandhi, A.; Akkary, H.; Rajwar, R.; Srinivasan, S.T.; Lai, K.;  
Computer Architecture, 2005. ISCA '05. Proceedings. 32nd International Symposium on  
4-8 June 2005 Page(s):446 - 457  
Digital Object Identifier 10.1109/ISCA.2005.46  
[AbstractPlus](#) | Full Text: [PDF](#)(192 KB) IEEE CNF
  
- ☐ **18. IEEE Standard for Modeling and Simulation [M and S] High Level Architecture [HLA] -  
Federate Interface Specification**  
IEEE Std 1516.1-2000  
2001 Page(s):i - 467  
[AbstractPlus](#) | Full Text: [PDF](#)(2276 KB) IEEE STD
  
- ☐ **19. Load-Store Queue Management: an Energy-Efficient Design Based on a State-Filtering  
Mechanism.**  
Castro, F.; Chaver, D.; Pinuel, L.; Prieto, M.; Tirado, F.; Huang, M.;  
Computer Design, 2005. Proceedings. 2005 International Conference on  
02-05 Oct. 2005 Page(s):617 - 624  
Digital Object Identifier 10.1109/ICCD.2005.70  
[AbstractPlus](#) | Full Text: [PDF](#)(472 KB) IEEE CNF
  
- ☐ **20. Memory ordering: a value-based approach**  
Cain, H.W.; Lipasti, M.H.;  
Computer Architecture, 2004. Proceedings. 31st Annual International Symposium on  
19-23 June 2004 Page(s):90 - 101  
Digital Object Identifier 10.1109/ISCA.2004.1310766  
[AbstractPlus](#) | Full Text: [PDF](#)(387 KB) IEEE CNF
  
- ☐ **21. Complexity-effective reorder buffer designs for superscalar processors**  
Kucuk, G.; Ponomarev, D.V.; Ergin, O.; Ghose, K.;  
Computers, IEEE Transactions on  
Volume 53, Issue 6, June 2004 Page(s):653 - 665  
Digital Object Identifier 10.1109/TC.2004.5  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(1376 KB) IEEE JNL
  
- ☐ **22. Fairness-guaranteed per-class-type queueing and hierarchical packet scheduling for  
DiffServ-aware-MPLS network**  
Chu Kim; Youngtak Kim; Montgomery, D.;  
Global Telecommunications Conference, 2004. GLOBECOM '04. IEEE  
Volume 3, 29 Nov.-3 Dec. 2004 Page(s):1718 - 1722 Vol.3  
Digital Object Identifier 10.1109/GLOCOM.2004.1378275  
[AbstractPlus](#) | Full Text: [PDF](#)(780 KB) IEEE CNF
  
- ☐ **23. Reducing reorder buffer complexity through selective operand caching**  
Kucuk, G.; Ponomarev, D.T.; Ergin, O.; Ghose, K.;  
Low Power Electronics and Design, 2003. ISLPED '03. Proceedings of the 2003 International  
Symposium on  
25-27 Aug. 2003 Page(s):235 - 240  
Digital Object Identifier 10.1109/LPE.2003.1231868  
[AbstractPlus](#) | Full Text: [PDF](#)(783 KB) IEEE CNF
  
- ☐ **24. CCL v3.0: multiprogrammed semi-asynchronous checkpoints**  
Quaglia, F.; Santoro, A.;  
Parallel and Distributed Simulation, 2003. (PADS 2003). Proceedings. Seventeenth Workshop  
on  
10-13 June 2003 Page(s):21 - 28  
Digital Object Identifier 10.1109/PADS.2003.1207417



**25. Execution cache-based microarchitecture for power-efficient superscalar processors**

Talpes, E.; Marculescu, D.;

Very Large Scale Integration (VLSI) Systems, IEEE Transactions on

Volume 13, Issue 1, Jan. 2005 Page(s):14 - 26

Digital Object Identifier 10.1109/TVLSI.2004.840406

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(632 KB\)](#) IEEE JNL

View: [1-25](#) | [26-50](#) | [51-75](#) | [76-100](#)

[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

© Copyright 2005 IEEE – All Rights Reserved


Terms used [queue](#) [commit](#) [index](#) [key](#)

Found 364 of 167,655

Sort results by


[Save results to a Binder](#)

Try an [Advanced Search](#)

Try this search in [The ACM Guide](#)

Display results


[Search Tips](#)
☐ Open results in a new window

Results 1 - 20 of 200

Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [next](#)

Best 200 shown

Relevance scale ☐ ☐ ☐ ☐ ☐

## 1 [Interposed request routing for scalable network storage](#)


February 2002 **ACM Transactions on Computer Systems (TOCS)**, Volume 20 Issue 1

Publisher: ACM Press

Full text available: pdf(363.12 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

This paper explores interposed request routing in Slice, a new storage system architecture for high-speed networks incorporating network-attached block storage. Slice interposes a request switching filter---called a  $\mu$ proxy---along each client's network path to the storage service (e.g., in a network adapter or switch). The  $\mu$ proxy intercepts request traffic and distributes it across a server ensemble. We propose request routing schemes for I/O and file service traffic, and explore th ...

**Keywords:** Content switch, file server, network file system, network storage, request redirection, service virtualization

## 2 [Events in Haskell, and how to implement them](#)



George Russell

October 2001 **ACM SIGPLAN Notices , Proceedings of the sixth ACM SIGPLAN international conference on Functional programming ICFP '01**, Volume 36 Issue 10

Publisher: ACM Press

Full text available: pdf(176.61 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We describe a new and simpler implementation in Haskell of CML's events, which encode reactions by a thread to combinations of messages from other threads. We add a new type of Guarded Events, by which recipients can filter messages with conditions on their value known as Guards. We implement guarded channels. The guard type and the indexing algorithm are not part of the channel definition, so that the user can trade off what guards are required against the cost of indexing. As an exampl ...

## 3 [ARIES: a transaction recovery method supporting fine-granularity locking and partial rollbacks using write-ahead logging](#)



C. Mohan, Don Haderle, Bruce Lindsay, Hamid Pirahesh, Peter Schwarz

March 1992 **ACM Transactions on Database Systems (TODS)**, Volume 17 Issue 1

Publisher: ACM Press

Full text available: pdf(5.23 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

DB2TM, IMS, and TandemTM systems. ARIES is applicable not only to database management systems but also to persistent object-oriented languages, recoverable file systems and transaction-based operating systems. ARIES has been implemented, to

varying degrees, in IBM's OS/2TM Extended Edition Database Manager, DB2, Workstation Data Save Facility/VM, Starburst and QuickSilver, and in the University of Wisconsin's EXODUS and Gamma d ...

**Keywords:** buffer management, latching, locking, space management, write-ahead logging

#### 4 Cheap recovery: a key to self-managing state



Andrew C. Huang, Armando Fox

February 2005 **ACM Transactions on Storage (TOS)**, Volume 1 Issue 1

**Publisher:** ACM Press

Full text available: pdf(1.24 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Cluster hash tables (CHTs) are key components of many large-scale Internet services due to their highly-scalable performance and the prevalence of the type of data they store. Another advantage of CHTs is that they can be designed to be as self-managing as a cluster of stateless servers. One key to achieving this extreme manageability is reboot-based recovery that is predictably fast and has modest impact on system performance and availability. This "cheap" recovery mechanism simplifies manage...

**Keywords:** Cluster hash table, manageability, quorum replication, storage systems design

#### 5 Efficient and flexible methods for transient versioning of records to avoid locking by read-only transactions



C. Mohan, Hamid Pirahesh, Raymond Lorie

June 1992 **ACM SIGMOD Record , Proceedings of the 1992 ACM SIGMOD international conference on Management of data SIGMOD '92**, Volume 21 Issue 2

**Publisher:** ACM Press

Full text available: pdf(1.19 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present efficient and flexible methods which permit read-only transactions that do not mind reading a possibly slightly old, but still consistent, version of the data base to execute without acquiring locks. This approach avoids the undesirable interferences between such queries and the typically shorter update transactions that cause unnecessary and costly delays. Indexed access by such queries is also supported, unlike by the earlier methods. Old versions of records are maintained only ...

#### 6 Efficient distributed recovery using message logging



A. P. Sistla, J. L. Welch

June 1989 **Proceedings of the eighth annual ACM Symposium on Principles of distributed computing**

**Publisher:** ACM Press

Full text available: pdf(1.87 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

#### 7 Research session: architectural issues: C-store: a column-oriented DBMS



Mike Stonebraker, Daniel J. Abadi, Adam Batkin, Xuedong Chen, Mitch Cherniack, Miguel Ferreira, Edmond Lau, Amerson Lin, Sam Madden, Elizabeth O'Neil, Pat O'Neil, Alex Rasin, Nga Tran, Stan Zdonik

August 2005 **Proceedings of the 31st international conference on Very large data bases VLDB '05**

**Publisher:** VLDB Endowment

Full text available: pdf(210.85 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper presents the design of a read-optimized relational DBMS that contrasts sharply with most current systems, which are write-optimized. Among the many differences in its design are: storage of data by column rather than by row, careful coding and packing of

objects into storage including main memory during query processing, storing an overlapping collection of column-oriented projections, rather than the current fare of tables and indexes, a non-traditional implementation of transactions ...

## 8 The family of concurrent logic programming languages



Ehud Shapiro

September 1989 **ACM Computing Surveys (CSUR)**, Volume 21 Issue 3

**Publisher:** ACM Press

Full text available: pdf(9.62 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Concurrent logic languages are high-level programming languages for parallel and distributed systems that offer a wide range of both known and novel concurrent programming techniques. Being logic programming languages, they preserve many advantages of the abstract logic programming model, including the logical reading of programs and computations, the convenience of representing data structures with logical terms and manipulating them using unification, and the amenability to metaprogramming ...

## 9 An object server for an object-oriented database system

Andrea H. Skarra, Stanley B. Zdonik, Stephen P. Reiss

September 1986 **Proceedings on the 1986 international workshop on Object-oriented database systems**

**Publisher:** IEEE Computer Society Press

Full text available: pdf(853.89 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This paper summarizes the interface, implementation, and use of a server process that is used as a backend by an object-oriented database system. This server is responsible for managing objects on secondary storage, managing transactions, and implementing a simple form of trigger. We sketch the interface of this system and point out some of the more interesting implementation issues that were encountered in building it. Client processes communicate asynchronously with the server ...

## 10 Parallel multisource view maintenance

Xin Zhang, Lingli Ding, Elke A. Rundensteiner

January 2004 **The VLDB Journal — The International Journal on Very Large Data Bases**, Volume 13 Issue 1

**Publisher:** Springer-Verlag New York, Inc.

Full text available: pdf(382.15 KB)

Additional Information: [full citation](#), [abstract](#), [index terms](#)

In a distributed environment, materialized views are used to integrate data from different information sources and then store them in some centralized location. In order to maintain such materialized views, maintenance queries need to be sent to information sources by the data warehouse management system. Due to the independence of the information sources and the data warehouse, concurrency issues are raised between the maintenance queries and the local update transactions at each information source so ...

**Keywords:** Concurrent data updates, Data warehousing, Parallel view maintenance, Performance evaluation

## 11 Practical byzantine fault tolerance and proactive recovery



Miguel Castro, Barbara Liskov

November 2002 **ACM Transactions on Computer Systems (TOCS)**, Volume 20 Issue 4

**Publisher:** ACM Press

Full text available: pdf(1.63 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Our growing reliance on online services accessible on the Internet demands highly available systems that provide correct service without interruptions. Software bugs, operator mistakes, and malicious attacks are a major cause of service interruptions and they can cause arbitrary behavior, that is, Byzantine faults. This article describes a new

replication algorithm, BFT, that can be used to build highly available systems that tolerate Byzantine faults. BFT can be used in practice to implement re ...

**Keywords:** Byzantine fault tolerance, asynchronous systems, proactive recovery, state machine replication, state transfer

12 Scalable Load and Store Processing in Latency Tolerant Processors

Amit Gandhi, Haitham Akkary, Ravi Rajwar, Srikanth T. Srinivasan, Konrad Lai

June 2005 **Proceedings of the 32nd Annual International Symposium on Computer Architecture ISCA '05**

**Publisher:** IEEE Computer Society

Full text available:  pdf(187.74 KB) Additional Information: [full citation](#), [abstract](#)

Memory latency tolerant architectures support thousands of in-flight instructions without scaling cycle-critical processor resources, and thousands of useful instructions can complete in parallel with a miss to memory. These architectures however require large queues to track all loads and stores executed while a miss is pending. Hierarchical designs alleviate cycle time impact of these structures but the CAM and search functions required to enforce memory ordering and provide data forwarding pl ...

13 Transient-fault recovery using simultaneous multithreading



T. N. Vijaykumar, Irith Pomeranz, Karl Cheng

May 2002 **ACM SIGARCH Computer Architecture News , Proceedings of the 29th annual international symposium on Computer architecture ISCA '02 , Proceedings of the 29th annual international symposium on Computer architecture ISCA '02**, Volume 30 Issue 2

**Publisher:** IEEE Computer Society, ACM Press

Full text available:  pdf(1.55 MB)  Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)  
[Publisher Site](#)

We propose a scheme for transient-fault recovery called **Simultaneously and Redundantly Threaded processors with Recovery (SRTR)** that enhances a previously proposed scheme for transient-fault detection, called Simultaneously and Redundantly Threaded (SRT) processors. SRT replicates an application into two communicating threads, one executing ahead of the other. The trailing thread repeats the computation performed by the leading thread, and the values produced by the two threads are compar ...

14 Research papers: storage, indexing, and system architecture: Online B-tree merging



Xiaowei Sun, Rui Wang, Betty Salzberg, Chendong Zou

June 2005 **Proceedings of the 2005 ACM SIGMOD international conference on Management of data**

**Publisher:** ACM Press

Full text available:  pdf(394.41 KB) Additional Information: [full citation](#), [abstract](#), [references](#)

Many scenarios involve merging of two B-tree indexes, both covering the same key range. Increasing demand for continuous availability and high performance requires that such merging be done online, with minimal interference to normal user transactions. In this paper we present an online B-tree merging method, in which the merging of leaf pages in two B-trees are piggybacked lazily with normal user transactions, thus making the merging I/O efficient and allowing user transactions to access only o ...

15 Parallelism in relational data base systems: architectural issues and design approaches



Hamid Pirahesh, C. Mohan, Josephine Cheng, T. S. Liu, Pat Selinger

July 1990 **Proceedings of the second international symposium on Databases in parallel and distributed systems**

**Publisher:** ACM Press

Full text available:  pdf(2.50 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

With current systems, some important complex queries may take days to complete because of: (1) the volume of data to be processed, (2) limited aggregate resources. Introducing parallelism addresses the first problem. Cheaper, but powerful computing resources solve the second problem. According to a survey by Brodie,<sup>1</sup> only 10% of computerized data is in data bases. This is an argument for both more variety and volume of data to be moved into data base systems. We conject ...

## 16 Computing curricula 2001



September 2001 **Journal on Educational Resources in Computing (JERIC)**

**Publisher:** ACM Press

Full text available: pdf(613.63 KB)

html(2.78 KB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



## 17 An analysis of a resource efficient checkpoint architecture



Haitham Akkary, Ravi Rajwar, Srikanth T. Srinivasan

December 2004 **ACM Transactions on Architecture and Code Optimization (TACO)**,

Volume 1 Issue 4

**Publisher:** ACM Press

Full text available: pdf(757.69 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Large instruction window processors achieve high performance by exposing large amounts of instruction level parallelism. However, accessing large hardware structures typically required to buffer and process such instruction window sizes significantly degrade the cycle time. This paper proposes a novel checkpoint processing and recovery (CPR) microarchitecture, and shows how to implement a large instruction window processor without requiring large structures thus permitting a high clock frequency ...

**Keywords:** Computer architecture, checkpoint architecture, high-performance computing, scalable architecture



## 18 Reducing Design Complexity of the Load/Store Queue

Il Park, Chong Liang Ooi, T. N. Vijaykumar

December 2003 **Proceedings of the 36th annual IEEE/ACM International Symposium on Microarchitecture**

**Publisher:** IEEE Computer Society

Full text available: pdf(174.73 KB)

Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

With faster CPU clocks and wider pipelines, all relevant microarchitecture components should scale accordingly. There have been many proposals for scaling the issue queue, register file, and cache hierarchy. However, nothing has been done for scaling the load/store queue, despite the increasing pressure on the load/store queue in terms of capacity and search bandwidth. The load/store queue is a CAM structure which holds in-flight memory instructions and supports simultaneous searches to honor memory dep ...



## 19 WISQ: a restartable architecture using queues



A. R. Pleszkun, J. R. Goodman, W. C. Hsu, R. T. Joersz, G. Bier, P. Woest, P. B. Schechter

June 1987 **Proceedings of the 14th annual international symposium on Computer architecture**

**Publisher:** ACM Press

Full text available: pdf(1.14 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

In this paper, the WISQ architecture is described. This architecture is designed to achieve high performance by exploiting new compiler technology and using a highly segmented pipeline. By having a highly segmented pipeline, a very-high-speed clock can be used. Since a highly segmented pipeline will require relatively long pipelines, a way must be provided to minimize the effects of pipeline bubbles that are formed due to data and control dependencies. It is also important to provide a way ...





## Multikey access methods based on superimposed coding techniques

R. Sacks-Davis, A. Kent, K. Ramamohanarao

November 1987 **ACM Transactions on Database Systems (TODS)**, Volume 12 Issue 4

**Publisher:** ACM Press

Full text available:  pdf(3.71 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Both single-level and two-level indexed descriptor schemes for multikey retrieval are presented and compared. The descriptors are formed using superimposed coding techniques and stored using a bit-inversion technique. A fast-batch insertion algorithm for which the cost of forming the bit-inverted file is less than one disk access per record is presented. For large data files, it is shown that the two-level implementation is generally more efficient for queries with a small number of matchin ...

Results 1 - 20 of 200

Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [next](#)

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2005 ACM, Inc.

[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)  [Real Player](#)

## Nothing Found

Your search for **+abstract:queue +abstract:commit +abstract:index +abstract:key** did not return any results.

You may want to try an [Advanced Search](#) for additional options.

Please review the [Quick Tips](#) below or for more information see the [Search Tips](#).

### Quick Tips

- Enter your search terms in lower case with a space between the terms.

sales offices

You can also enter a full question or concept in plain language.

Where are the sales offices?

- Capitalize proper nouns to search for specific people, places, or products.

John Colter, Netscape Navigator

- Enclose a phrase in double quotes to search for that exact phrase.

"museum of natural history" "museum of modern art"

- Narrow your searches by using a **+** if a search term must appear on a page.

museum +art

- Exclude pages by using a **-** if a search term must not appear on a page.

museum -Paris

Combine these techniques to create a specific search query. The better your description of the information you want, the more relevant your results will be.

museum +"natural history" dinosaur -Chicago

## Nothing Found

Your search for **+review:queue +review:commit +review:index +review:key** did not return any results.

You may want to try an [Advanced Search](#) for additional options.

Please review the [Quick Tips](#) below or for more information see the [Search Tips](#).

## Quick Tips

- Enter your search terms in lower case with a space between the terms.

sales offices

You can also enter a full question or concept in plain language.

Where are the sales offices?

- Capitalize proper nouns to search for specific people, places, or products.

John Colter, Netscape Navigator

- Enclose a phrase in double quotes to search for that exact phrase.

"museum of natural history" "museum of modern art"

- Narrow your searches by using a + if a search term must appear on a page.

museum +art

- Exclude pages by using a - if a search term must not appear on a page.

museum -Paris

Combine these techniques to create a specific search query. The better your description of the information you want, the more relevant your results will be.

museum +"natural history" dinosaur -Chicago

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2005 ACM, Inc.

[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)  [Real Player](#)

## Nothing Found

Your search for **+title:queue +title:commit +title:index +title:key** did not return any results.

You may want to try an [Advanced Search](#) for additional options.

Please review the [Quick Tips](#) below or for more information see the [Search Tips](#).

## Quick Tips

- Enter your search terms in lower case with a space between the terms.

sales offices

You can also enter a full question or concept in plain language.

Where are the sales offices?

- Capitalize proper nouns to search for specific people, places, or products.

John Colter, Netscape Navigator

- Enclose a phrase in double quotes to search for that exact phrase.

"museum of natural history" "museum of modern art"

- Narrow your searches by using a **+** if a search term must appear on a page.

museum +art

- Exclude pages by using a **-** if a search term must not appear on a page.

museum -Paris

Combine these techniques to create a specific search query. The better your description of the information you want, the more relevant your results will be.

museum +"natural history" dinosaur -Chicago


Terms used [queue](#) [commit](#) [index](#)

Found 1 of 167,655

Sort results by


[Save results to a Binder](#)

Try an [Advanced Search](#)

Try this search in [The ACM Guide](#)

Display results


[Search Tips](#)
☐ Open results in a new window

Results 1 - 1 of 1

Relevance scale ☐ ☐ ☐ ☐ ☐

## 1 [Scalable Store-Load Forwarding via Store Queue Index Prediction](#)



Tingting Sha, Milo M. K. Martin, Amir Roth

November 2005 **Proceedings of the 38th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO'05) - Volume 00 MICRO '05**

Publisher: IEEE Computer Society

Full text available:


[Publisher Site](#)

Additional Information: [full citation](#), [abstract](#)

Conventional processors use a fully-associative store queue (SQ) to implement store-load forwarding. Associative search latency does not scale well to capacities and bandwidths required by wide-issue, large window processors. In this work, we improve SQ scalability by implementing store-load forwarding using speculative indexed access rather than associative search. Our design uses prediction to identify the single SQ entry from which each dynamic load is most likely to forward. When a load exec ...

Results 1 - 1 of 1

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2005 ACM, Inc.

[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads: [Adobe Acrobat](#) [QuickTime](#) [Windows Media Player](#) [Real Player](#)


[Web](#) [Images](#) [Groups](#) [News](#) [Froogle](#) [Local](#)<sup>New!</sup> [more »](#)

queue commit index

Search

[Advanced Search](#)  
[Preferences](#)

## Web

Results 1 - 10 of about **499,000** for **queue commit index**. (0.15 seconds)

## Contents

... listen -- Complete binding, create connection request **queue** · lockc-Lock a resource ... TO2\_addRecoupIndexEntry-Add an entry to a recoup **index** ...  
 publib.boulder.ibm.com/infocenter/tpfhelp/ current/topic/com.ibm.ztpf.doc\_put.01/gtpc2/gtpc2m02.htm - 99k - [Cached](#) - [Similar pages](#)

PHPXRef 0.5 : Xaraya : Full Variable **Index**

... \$comments Definitions: 1 References: 4; \$commit Definitions: 1 ... \$queue Definitions: 13 References: 29; \$quote Definitions: 7 References: 29 ...  
 www.xaraya.com/documentation/phpxref/\_variables/ - 513k - [Cached](#) - [Similar pages](#)

PHPXRef 0.4 : PostNuke .80 : Full Variable **Index**

... \$commit Definitions: 2 References: 4; \$compare2crypt Definitions: 2 ... \$queue Definitions: 3 References: 35; \$quote Definitions: 8 References: 23 ...  
 docs.markwest.me.uk/phpxref/pn80/\_variables/ - 408k - [Cached](#) - [Similar pages](#)

## Mainframe Week - Code mq

PRIMARY KEY (QSGNAME) ) IN MQDB1.MQTS1; CREATE TYPE 2 UNIQUE **INDEX** CSQ.ADMIN\_QSG ON CSQ. ... Repeat for each **queue** manager in the QSG (here MQT1 and MQT2). ...  
 www.mainframeweek.com/ code/showcode.php/0044/mw44mq1.txt - 13k - [Cached](#) - [Similar pages](#)

[xiph-cvs] cvs **commit**: vorbis-plugins/realplayer/render **queue**.cpp ...

[xiph-cvs] cvs **commit**: vorbis-plugins/realplayer/render **queue**.cpp **queue**.h ...  
 1.3 +16 -0 vorbis-plugins/realplayer/filefmt/fvorbis.h **Index**: fvorbis.h ...  
 lists.xiph.org/pipermail/commits/2001-April/000647.html - 13k - [Cached](#) - [Similar pages](#)

[xiph-cvs] cvs **commit**: vorbis-plugins/realplayer/render/make linux ...

jack 01/07/08 15:17:50 Modified: realplayer/render **queue**.cpp **queue**.h ...  
 Changes Path 1.2 +7 -7 vorbis-plugins/realplayer/render/**queue**.cpp **Index**: **queue**.cpp ...  
 lists.xiph.org/pipermail/commits/2001-July/000769.html - 7k - [Cached](#) - [Similar pages](#)

## jGuru: seeing this error in console

EOFException at com.swiftmq.impl.store.standard.**index**.PageOutputStream.c(Unknown ...  
 ... MessageQueue.**commit**(Unknown Source) at com.swiftmq.swiftlet.**queue**. ...  
 www.jguru.com/forums/view.jsp?EID=1267349 - 27k - [Cached](#) - [Similar pages](#)

[Xcb-**commit**] xcb/doc/tutorial **index**.html,1.2,1.3

[Xcb-**commit**] xcb/doc/tutorial **index**.html,1.2,1.3 ... It looks at the event + **queue** and returns (and dequeues too) an existing event into + a newly allocated ...  
 lists.freedesktop.org/archives/ xcb-**commit**/2005-July/000037.html - 8k - [Cached](#) - [Similar pages](#)

Kevin Buettner - [**commit**] Handle LWPs that have died without ...

**Index** Nav:, [Date **Index**] [Subject **Index**] [Author **Index**] [Thread **Index**] ...  
 [**commit**] Handle LWPs that have died without leaving a status ...  
 sources.redhat.com/ml/rda/2005-q4/msg00005.html - 7k - [Cached](#) - [Similar pages](#)

[PDF] Scalable Store-Load Forwarding via Store **Queue Index** Prediction (a ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)  
 Scalable Store-Load Forwarding via Store **Queue Index** Prediction ... SVW /  
 RE-EXECUTE / **COMMIT**. Associative store **queue** with original Store Sets scheduling ...  
 www.cis.upenn.edu/acg/papers/micro05\_storeq.pdf - [Similar pages](#)

Try searching for queue commit index on Google Book Search

Goooooooooooooogle ►

Result Page:    1 2 3 4 5 6 7 8 9 10    **Next**



Free! Instantly find your email, files, media and web history. [Download now.](#)

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2005 Google

## Book Search

Books 1 - 10 with 64 pages on **queue commit index**. (0.22 seconds)



### Transactional Information Systems: Theory, Algorithms, and the Practice of Concurrency Control...

by Gottfried Vossen, Gerhard Weikum - Computers - 2001 - 852 pages

Page 350 - The bookkeeping for this kind of situation amounts to managing a **queue** of lock control blocks ( ... Then, upon the **commit** or abort of the transaction, ...

[ [More results from this book](#) ]



### Databases in Telecommunications

by Willem Jonker - Technology - 2000 - 217 pages

Page 182 - Also implements a library of **index** classes for the hash table, T-tree [14], ...

Client Requests Transaction Scheduler Input **Queue** Database Manager, ...

[ [More results from this book](#) ]



### Oracle Database 10G New Features: Oracle 10g Reference for Advanced Tuning & Administration

by Mike Ault, Madhu Tamma, Daniel Liu - 2003 - 528 pages

Page 412 - Let us see an example showing how to extract **Commit** SCN, object owner, ...

**Index**-organized tables (JOT) are also now supported, with the following ...

[ [More results from this book](#) ]



### Oracle Privacy Security Auditing: Includes Federal Law Compliance with Hipaa, Sarbanes Oxley...

by Arup Nanda, Donald K Burleson - Computers - 2003 - 655 pages

Page 431 - ... GRANT TYPE **INDEX** INSERT ANY TABLE INSERT TABLE LOCK ANY TABLE

LOCK TABLE MANAGE ANY MANAGE MATERIALIZED **QUEUE** TABLESPACE VIEW ON **COMMIT** ...

[ [More results from this book](#) ]



### Hardware and Software Architectures for Fault Tolerance: Experiences and Perspectives

Computers - 1994 - 311 pages

Page 52 - 52 4 Implicit **Index** Schedule Reconstruction Implicit **index** scheduling supports

... instructions in a FIFO **queue** called a speculation read buffer (SRB). ...

[ [More results from this book](#) ]

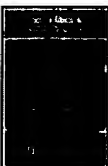


### Principles & Implementation of Datawarehousing

Page 167 - Furthermore, for refresh ON **COMMIT**, Oracle keeps track of the type of DML ...

Set the number of job **queue** processes greater than the number of processors. ...

[ [More results from this book](#) ]



### Database Machines: Sixth International Workshop, Iwdm '89, Deauville, France, June 19-21,...

edited by Haran Boral, Pascal Faudemay - 1989 - 393 pages

Page 41 - ... of receiving thread send cost includes enqueueing message on receiver's **queue**,

... At **commit** time, the TC in charge of the transaction would activate a ...

[ [More results from this book](#) ]



### Dictionary of Electrical and Computer Engineering

by McGraw-Hill - 2004

Page 274 - ... 5db A **queue** consisting of jobs that have been submitted for execution by a

... (**coMmiT** sd) A sequence of characters preceded by an H and a character ...

[ [More results from this book](#) ]



### Mobile Agents

by Kurt Rothermel, Fritz Hohl - Computers - 1998 - 292 pages

Page 24 - Furthermore, the information from the input **queue** is processed in such a way that ... leader will be able to **commit** another terminating stage transaction. ...

[ [More results from this book](#) ]



### Los Angeles Times Crosswords: 72 Puzzles from the Daily Paper

edited by Rich Norris - Games - 2004 - 96 pages

Page 69 - ... protagonist 17 TAPS 19 **Commit** a faux pas 20 Prep for a long run, ...  
33 Embroidered mat 34 Join a **queue** 36 Wall St. **index** 38 Go across 39 Winter racing ...

[ [More results from this book](#) ]

Goooooogle ►

Result Page:    1   2   3   4   5    [Next](#)

queue commit index

Search all books

Search the Web

[Google Book Search Help](#)

[About Google Book Search](#) - [Information for Publishers](#) - [Google Home](#)

©2005 Google

 **OPTION 1**  
**Quick Find an Author:**  
Enter a name to locate articles written by that author.

No Authors found beginning with letter: **kettley**



Example: Enter Lockett S to obtain a list of authors with the last name Lockett and the first initial S.

 **OPTION 2**  
**Browse alphabetically**

Select a letter from the list.

[A](#) [B](#) [C](#) [D](#) [E](#) [F](#) [G](#) [H](#) [I](#) [J](#) [K](#) [L](#) [M](#) [N](#) [O](#) [P](#) [Q](#) [R](#) [S](#) [T](#) [U](#) [V](#) [W](#) [X](#) [Y](#) [Z](#)



**OPTION 1**

**Quick Find an Author:**

Enter a name to locate articles written by that author.



Example: Enter Lockett S to obtain a list of authors with the last name Lockett and the first initial S.

Select a name to view articles written by that author

[Warnes A. M.](#)

[Warnes G.](#)

[Warnes P. N.](#)

[Warnes W.](#)

[Warnes W. H.](#)



**OPTION 2**

**Browse alphabetically**

Select a letter from the list.

**[A](#) [B](#) [C](#) [D](#) [E](#) [F](#) [G](#) [H](#) [I](#) [J](#) [K](#) [L](#) [M](#) [N](#) [O](#) [P](#) [Q](#) [R](#) [S](#) [T](#) [U](#) [V](#) [W](#) [X](#) [Y](#) [Z](#)**

Indexed by



[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

© Copyright 2005 IEEE – All Rights Reserved

**Search Results**

**BROWSE**

**SEARCH**

**IEEE XPLORE GUIDE**

**SUPPORT**

Results for "(hopewell p.<in>au)"

Your search matched **4** of **1263585** documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

 [e-mail](#)  [printer friendly](#)

» **Search Options**

[View Session History](#)

[New Search](#)

**Modify Search**

(hopewell p.<in>au)



☐ Check to search only within this results set

Display Format: ☒ Citation ☐ Citation & Abstract

» **Key**

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

**Select Article Information**

- ☐ 1. **Anomalous dielectric response of very small quantities of virgin, aged and failed silicone oil**  
Haider, A.; Fothergill, J.C.; Dissado, L.A.; Hopewell, P.;  
Dielectrics and Electrical Insulation, IEEE Transactions on [see also Electrical Insulation, IEEE Transactions on]  
Volume 10, Issue 2, April 2003 Page(s):336 - 342  
Digital Object Identifier 10.1109/TDEI.2003.1194120  
[AbstractPlus](#) | Full Text: [PDF\(535 KB\)](#) IEEE JNL
- ☐ 2. **Costs of sustainable electricity generation**  
Newton, M.J.; Hopewell, P.D.;  
Power Engineering Journal [see also Power Engineer]  
Volume 16, Issue 2, April 2002 Page(s):68 - 74  
[AbstractPlus](#) | Full Text: [PDF\(454 KB\)](#) IEE JNL
- ☐ 3. **Costs of sustainable electricity generation**  
Newton, M.J.; Hopewell, P.D.;  
Engineering Science and Education Journal  
Volume 11, Issue 2, April 2002 Page(s):49 - 55  
[AbstractPlus](#) | Full Text: [PDF\(492 KB\)](#) IEE JNL
- ☐ 4. **Loss-of-mains detection for small generators**  
Hopewell, P.D.; Jenkins, N.; Cross, A.D.;  
Electric Power Applications, IEE Proceedings-  
Volume 143, Issue 3, May 1996 Page(s):225 - 230  
[AbstractPlus](#) | Full Text: [PDF\(512 KB\)](#) IEE JNL

